

WEST Search History

DATE: Thursday, April 08, 2004

Hide?	Set Name	Query	Hit Count
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L8	L7.ti.	36
<input type="checkbox"/>	L7	configur\$ near5 non\$1volatile	2131
<input type="checkbox"/>	L6	strap\$ near3 non\$1volatile	15
<input type="checkbox"/>	L5	strap\$ near3 non\$1volatile near10 configur\$	0
<input type="checkbox"/>	L4	L3 and strap\$	2
<input type="checkbox"/>	L3	L2 or l1	223
<input type="checkbox"/>	L2	elamin-\$xp.	35
<input type="checkbox"/>	L1	elamin-\$xa.	188

END OF SEARCH HISTORY

First Hit Fwd Refs

Generate Collection

L6: Entry 9 of 15

File: USPT

Jul 31, 2001

DOCUMENT-IDENTIFIER: US 6269443 B1

TITLE: Method and apparatus for automatically selecting CPU clock frequency multiplier

Brief Summary Text (8):

The jumper solution has a disadvantage in that if the jumpers are incorrectly configured, the processor will either fail or run too slowly. The only way to correct the problem is for a human being to change the jumper configuration. The non-volatile memory solution has the disadvantage of adding the cost of the non-volatile memory device to the cost of the system. With the non-volatile solution, if the processor fails to function correctly with the current strapping settings, the non-volatile memory must be reprogrammed. Both the jumper solution and the non-volatile memory solution incur the cost of the MUX.